Abstract of the Disclosure

A microstrip patch array antenna suppressing side The microstrip patch array antenna have a plurality of antenna array elements on two-dimensional planar having A axis and B axis for suppressing side lobes, wherein the antenna array elements are linearly arranged in a direction of the A axis by spacing a first predetermined distance between the antenna array elements, the arranged array elements are arranged in a direction of the B axis by spacing a second predetermined distance between the antenna array elements and a predetermined portion microstrip patch array antenna having the arranged array horizontally shifted to a predetermined elements are The present invention can reduce leakage of distance. signal or prevent to receive undesired signal and to transmit signals to undesired direction by using the above mentioned array pattern instead of reducing a distance of spacing between antenna elements.

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